

Docket No. 240471US2S

IN RE APPLICATION OF: Kenji TSUCHIDA, et al.

SERIAL NO: 10/621,886

FILED: July 18, 2003

FOR: MRAM HAVING MEMORY CELL ARRAY IN WHICH CROSS-POINT MEMORY CELLS ARE ARRANGED BY HIERARCHICAL BIT LINE SCHEME AND DATA READ METHOD THEREOF

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Transmitted herewith is an amendment in the above-identified application.

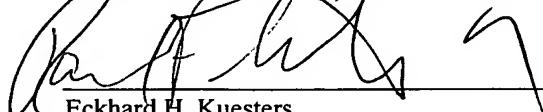
- No additional fee is required
- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	27	MINUS	29	0	x \$18 =	\$0.00
INDEPENDENT	6	MINUS	4	2	x \$88 =	\$176.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$300 =	\$0.00
						TOTAL OF ABOVE CALCULATIONS
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$176.00
		<input type="checkbox"/> Recordation of Assignment			+ \$40 =	\$0.00
						TOTAL
						\$176.00

- A check in the amount of \$0.00 is attached.
- Credit card payment form is attached to cover the fees in the amount of \$176.00
- Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

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DOCKET NO: 240471US2S



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

KENJI TSUCHIDA, ET AL.

: EXAMINER: TRAN, MICHAEL THANH

SERIAL NO: 10/621,886

:

FILED: JULY 18, 2003

: GROUP ART UNIT: 2818

FOR: MRAM HAVING MEMORY CELL  
ARRAY IN WHICH CROSS-POINT  
MEMORY CELLS ARE ARRANGED BY  
HIERARCHICAL BIT LINE SCHEME  
AND DATA READ METHOD THEREOF

AMENDMENT

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated August 17, 2004, please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 5 of this paper.

**Remarks** begin on page 16 of this paper.

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